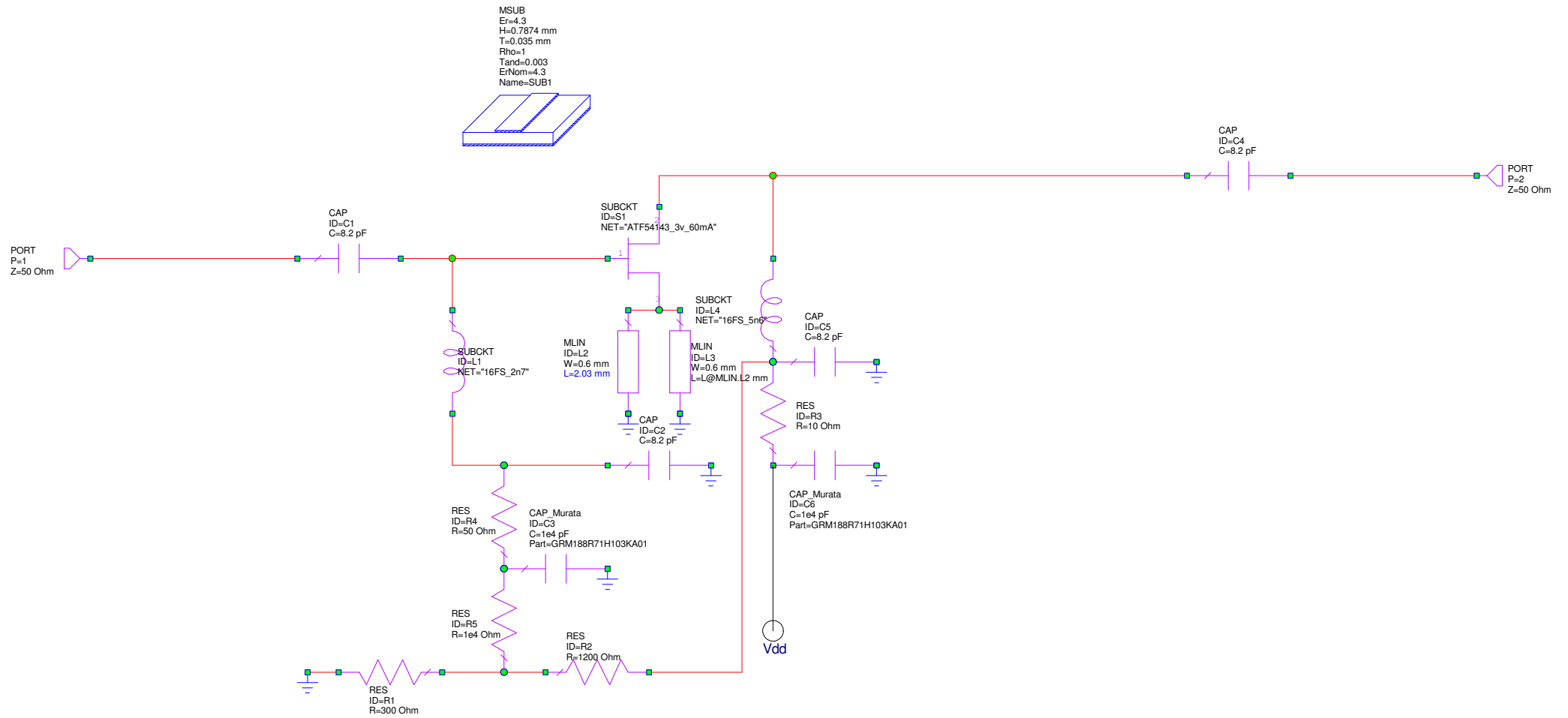




The Idealised Circuit From Agilent's Data Sheet